

**10/591178**

**IAP5 Rec'd PCT/PTO 30 AUG 2006**  
**PATENT**

**Docket No.: 4590-556**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	
	:	
Pierre BLANCHARD	:	Confirmation No.
	:	
U.S. Patent Application No.	:	Group Art Unit:
	:	
Filed: herewith	:	Examiner:

**For: INTEGRATED CIRCUIT WITH A VERY SMALL-SIZED READING DIODE**

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Preliminary to examination of the above-referenced application, please amend the application as follows: